

Start

Setup Cam Mode

Take Image

Get Frame

End

Take Image

Change to RegPage 2

Write 2 I2C Commands

End

Setup Cam Mode

Until End of Command Array

Is Current RegPage = Previous RegPage

?

Change RegPage

Send I2C Command

End of Command Array

?

End

Next Triple in Array

Get Frame

Clear SRAM Addr Counter

Output SRAM Addr

Wait for FV To Go High

Wait for LV To Go High

Wait for PXCLK To Go High

Sramd = CAMDAT

ceweoelbub = 00100

ceweoelbub = 01100

Increment SRAM Addr

Is SRAM Addr = 0xZZ ?

End

Possible speedups
 Start at top of SRAM
 DECRSZ
 Dual Port SRAM
 Use Tristate buffers
 Wishbone & CAM
 CAMCNTRL
 Use !PXCLK as WE
 As long as FV & LV
 Falling edge PXCLK incr sramaddr